

Case No: F0588

PATENT

CERTIFICATE OF MAILING OR FACSIMILE TRANSMISSION UNDER 37 CFR 1.8(a)

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Commissioner for Patents address below.

X being transmitted via facsimile to (703) 872-9306 at the U.S. Patent and Trademark Office to the Attention of Examiner Laura M. Schillinger.



Mark D. Saralino

October 10, 2003

Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**RECEIVED
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In re application of: Qi Xiang et al.

Serial No.: 09/902,429

Filing Date: July 10, 2001

For: SILICON ON INSULATOR FIELD EFFECT TRANSISTOR WITH
HETEROJUNCTION GATE

Examiner: Laura M. Schillinger

Art Unit: 2813

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION DATED OCTOBER 3, 2003

Sir:

Favorable reconsideration of the above-identified application is respectfully requested in view of the following comments.